



### REMARKS

Claims 1-23 are pending in the above-referenced patent application. In this response, no claims are added, cancelled or amended.

In the Final Office Action, dated November 29, 2005, the Examiner rejected claims 1-4, 10-13 and 18-21 under 35 U.S.C. 102(b) as being anticipated by Hasegawa et al. (U.S. Patent No. 5,144,117, hereinafter "Hasegawa"); and rejected claims 5-6, 14-15 and 22-23 under 35 U.S.C 103(a) as being unpatentable under Hasegawa in view of McMahan et al. (U.S. Patent No. 4,504,951, hereinafter "McMahan"). These rejections are respectfully traversed.

Assignee respectfully submits that Hasegawa does not disclose each and every element of the rejected claims, and, therefore, a *prima facie* case under 35 U.S.C. 102(b) has not been established. As just an example, Hasegawa does not show or describe "a first lamp driving circuit for receiving the modulated voltage and driving a first lamp, a second lamp driving circuit for receiving the modulated voltage and driving a second lamp, and a reciprocal control circuit for sending the modulated voltage to the first lamp driving circuit or the second lamp driving circuit according to the dictate of a reciprocal logic signal." As recited in claim 1.

Hasegawa is directed toward an optical recorded information reading device comprising a plurality of LEDs, wherein the LEDs are driven by a current provided by a circuit. As stated from col 7:48 – 7:53, "the circuit 30 supplied a current of a constant amperage to the light source 18 consisting of an LED array to be hereinafter described. Accordingly, the light source 18 uniformly illuminates an area of the bar code 17 on the bar code media 16 with constant light emission intensity." Hasegawa does not show or describe the capability of driving a first or a second lamp, but, rather, shows and describes only the capability to drive all the lamps of the LED array.

Assignee respectfully submits that because Hasegawa does not disclose each and every element of the rejected claims, a *prima facie* case under 35 U.S.C. 102(b) has not been established, and claim 1 is in condition for allowance. It is noted that many other bases for traversing the rejection

could be provided, but Assignee believes that this ground is sufficient. Additionally, claims 2-4, 10-13 and 18-21 are in a condition for allowance for at least the same reasons as claim 1, as amended.

Assignee respectfully submits that claims 5-6, 14-15 and 22-23 are not rendered obvious by the cited art, Hasegawa in view of McMahan. As stated previously, Hasegawa does not show or describe "a first lamp driving circuit for receiving the modulated voltage and driving a first lamp, a second lamp driving circuit for receiving the modulated voltage and driving a second lamp, and a reciprocal control circuit for sending the modulated voltage to the first lamp driving circuit or the second lamp driving circuit according to the dictate of a reciprocal logic signal." As recited in claim 5, And McMahan fails to cure this deficiency. McMahan is directed toward high speed switching power supplies, and does not show or describe the capability of driving a first or a second lamp.

Assignee respectfully submits that, for at least the reasons presented above, a prima facie case of obviousness has not been established, and all pending claims are in a condition for allowance. It is noted that many other bases for traversing the rejection could be provided, but Assignee believes that this ground is sufficient.

**CONCLUSION**

In view of the foregoing, it is respectfully submitted that all of the claims pending in this patent application, as amended, are in condition for allowance. If the Examiner has any questions, she is invited to contact the undersigned at (503) 439-6500. Reconsideration of this patent application and early allowance of all the claims is respectfully requested.

Please charge any shortages and credit any overcharges of any fees required for this submission to Deposit Account number 50-3703.

Respectfully submitted,

Dated: \_\_\_\_\_

1/25/06



Michael J. Willardson  
Patent Attorney  
Reg. No. 50,856

Berkeley Law and Technology Group  
1700 NW 167<sup>th</sup> Place, Suite 240  
Beaverton, Oregon 97006  
Phone: 503-439-6500